	Application No.	Applicant(s)
Notice of Allowability	10/780,030	PARK, JONGMIN
	Examiner	Art Unit
	David Lam	2027
	David Lam	2827 .
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in to or other appropriate communication. This application is su	this application. If not included nication will be mailed in due course. THIS
1. $igspace$ This communication is responsive to <u>preamendment file on</u>	n 4/26/05.	
2. X The allowed claim(s) is/are 1-64.		
3. \boxtimes The drawings filed on <u>26 April 2005</u> are accepted by the E	xaminer.	
 4. ☐ Acknowledgment is made of a claim for foreign priority up a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application	No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	MENT of this application.	
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv		
6. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.	
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Review	(PTO-948) attached
1) hereto or 2) to Paper No./Mail Date	•	
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment or i	n the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the header according to 37 CFR	e drawings in the front (not the back) of 1.1.121(d).
7. DEPOSIT OF and/or INFORMATION about the depo		
Attachment(s)	_	
1. Notice of References Cited (PTO-892)	-	ormal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Sur Paper No./N	mmary (P1O-413), fail Date
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 7/26/04		mendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	-	Statement of Reasons for Allowance
of Biological Material	9. 🗌 Other	
		417
		121
		DAVID ĽAM PRIMARY EXAMINER

Response to Pre-Amendment

- 1. This office action is in response to pre-amendment file on 4/26/05.
 - Claims 51-64 are newly added.
 - Claims 1-64 are pending in the application.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

IN THE CLAIM:

- In claim 27, line 1; change "A electronic" to -- An electronic --.

Allowable Subject Matter

3. The following is an examiner's statement of reasons for allowance: Claims 1-64 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: An electronic circuit/EPROM comprising: a floating gate FET, a condition circuitry, among others as claimed in independence claims 27, 43, for selectively placing a first body and first control voltages to respective body and control conditioning values different from each other such that the second body and second control voltages cause the FET to be in the first

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condition/erase condition with it programmable threshold voltage (a) less than V_{T1} if the FET is n-channel type or (b) greater than -V_{T1} if the FET is p-channel type regardless of whether the FET was immediately previously in the first or second/erased or programmed condition. Method for controlling threshold voltage of a floating FET comprising step of subsequently discharging the first body and first control voltages to respective body and control discharge values between the conditioning values, and among others as claimed in independent claims 1, 19. A method of fabrication comprising step of providing a condition circuitry, among others as claimed in independence claims 27, 43, for selectively placing a first body and first control voltages to respective body and control conditioning values different from each other such that the second body and second control voltages cause the FET to be in the first condition/erase condition with it programmable threshold voltage (a) less than V_{T1} if the FET is n-channel type or (b) greater than -V_{T1} if the FET is p-channel type regardless of whether the FET was immediately previously in the first or second/erased or programmed condition, and among others as claimed in independent claims 61, 63.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's 4. disclosure.

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- Sali et al. (5,999,456) disclose a flash EEPROM with controlled discharge time of the word lines and source potentials after erase.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

PRIMARY EXAMINER

May 26, 2005